



06-02-03

GRAU 2829

EV085413549

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/092,875
	Filing Date	March 5, 2002
	First Named Inventor	Paul A. Morgan
	Group Art Unit	2829
	Examiner Name	Lisa A. Kilday
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1798

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Mark S. Matkin, Reg. No. 32,268 Wells St. John P.S.
Signature	
Date	5-29-03

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/092,875
Filing DateMarch 5, 2002
Inventor..... Paul A. Morgan et al.
Assignee Micron Technology, Inc.
Group Art Unit 2829
Examiner Lisa A. Kilday
Attorney's Docket No. MI22-1798
Title: Etching Processes for Integrated Circuit Manufacturing Including
Methods of Forming Capacitors

3/ Elect
G. Stanley
6-4-03

RESPONSE TO MAY 7, 2003, OFFICE ACTION

To: Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

VIA EXPRESS MAIL

From: Mark Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Responsive to the Office Action dated May 7, 2003, Applicant remarks
as follows:

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